Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	19	clock with strobe with net	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 11:55
L2	6222	clock near4 strobe	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:08
L3	5680	synchronous near4 interface	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:12
L4	299	2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 11:56
L5	10766	transmit\$3 same receiv\$3 same enabl\$3 same bit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 11:57
L6	77	4 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 11:57
L7	41	(macro or macrocell or (macro adj cell)) and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:11
L8	41	(bit with pin) and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:10
L9	11960	clock with strobe	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:09
L10	493	9 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:09
L11	115	5 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:09

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L12	46	(macro or macrocell or (macro adj cell)) and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:09
L13	46	(macro or macrocell or (macro adj cell) or hard-macro\$5 or (hard adj macro\$5) or macro\$5) and 11	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:12
L14	674	(macro or macrocell or (macro adj cell) or hard-macro\$5 or (hard adj macro\$5) or macro\$5) and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:13
L15	177	(synchronous with interface) and 14	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR .	ON	2006/10/04 12:12
L16	142	(bit with pin) and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:13
L17	5	("716"/\$.ccls. or "703"/\$.ccls.) and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 12:59
L18	350	plac\$4 same rout\$3 same (input/output or IO or I/O) same buffer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:17
L19	18	(clock near4 strobe) and 18	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:17
L20	8	(skew with data with clock) and 19	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:07
L21	10	(skew with data with clock) and 18	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:32
L22	21	((double with data with rate) or DDR or SDR or (single with rate with data)) and 18	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:09
L23	4	(bit with pin) and 22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:10

LAST Scarcif History							
L24	845	(plac\$4 with rout\$3) and ((double with data with rate) or DDR or SDR or (single with rate with data))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:16	
L25	76	(clock near4 strobe) and 24	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:32	
L26	14	((input/output or IO or I/O) with buffer) and 25	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:24	
L27	12	(enabl\$3 near4 bit) and 26	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:25	
L28	2	(enabl\$3 near4 bit) and (receiv\$3 near4 bit) and (transmit\$4 near4 bit) and 27	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:31	
L29	572	(plac\$4 with rout\$3) and (enabl\$3 near4 bit) and (receiv\$3 near4 bit) and (transmit\$4 near4 bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:33	
L30	50	(plac\$4 with rout\$3) and ((enabl\$3 near4 bit) same (receiv\$3 near4 bit) same (transmit\$4 near4 bit))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:31	
L31	6	(clock near4 strobe) and 30	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:32	
L32	2	(skew with data with clock) and 30	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 13:38	
L33	0	((plac\$4 with rout\$3) and (enabl\$3 near4 bit) and (receiv\$3 near4 bit) and (transmit\$4 near4 bit)).clm.	US-PGPUB	OR	ON	2006/10/04 13:34	
L34	35	((enabl\$3 near4 bit) and (receiv\$3 near4 bit) and (transmit\$4 near4 bit)).clm.	US-PGPUB	OR	ON	2006/10/04 13:37	
L35	4	rout\$3.clm. and 34	US-PGPUB	OR	ON	2006/10/04 13:38	
L36	1	(input/output or IO or I/O).clm. and 35	US-PGPUB	OR	ON	2006/10/04 13:38	
L37	2	(input/output or IO or I/O).clm. and 34	US-PGPUB	OR	ON	2006/10/04 13:36	

L38	154	((enabl\$3 with bit) and (receiv\$3 with bit) and (transmit\$4 with bit)). clm.	US-PGPUB	OR	ON	2006/10/04 13:37
L39	12	rout\$3.clm. and 38	US-PGPUB	OR	ON	2006/10/04 13:38
L40	1	(input/output or IO or I/O).clm. and 39	US-PGPUB	OR	ON	2006/10/04 13:38
L41	693	716/17.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 14:57
L42	636	716/16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 15:08
L43	230	716/15.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 15:13
L44	481	716/14.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/10/04 15:13